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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Hiroyuki YOSHIDA et al.

Group Art Unit: 2858

Application No.: 10/825,377

Examiner: Unknown

Filed: April 16, 2004

Docket No.: 119331

For: ELECTROSTATIC CAPACITANCE DETECTING DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

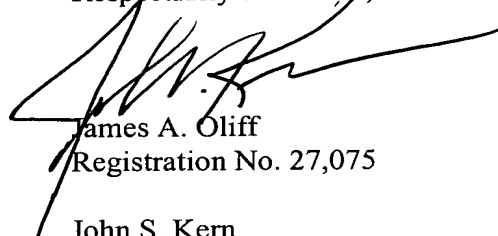
Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application, and this communication was not received by any individual designated in §1.56(c) more than thirty days prior to the filing of this Information Disclosure Statement.
- ☒ 3. The references were cited in a counterpart foreign application. An English language version of the foreign search report is attached for the Examiner's information.

- ☒ 4. In accordance with 37 CFR §1.98(a)(2)(ii), copies of any U.S. patents and patent application publications are not attached.

Respectfully submitted,



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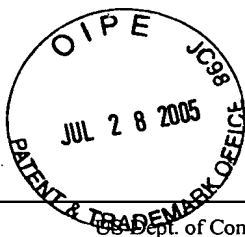
JAO:JSK/kap

Date: July 28, 2005

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Sheet 1 of 1

Form PTO-1449 (REV. 8-83)		U.S. Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 119331		APPLICATION NO. 10/825,377	
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)				APPLICANTS Hiroyuki YOSHIDA et al.			
				FILING DATE April 16, 2004		GROUP 2858	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	
	1	US 6,411,727 B1	6/25/2002	Harkin			
	2	6,049,620	4/11/2000	Dickinson et al.			
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	
	3	WO 01/06448 A1	1/25/2001	WIPO			
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
	4	Hashido, Ryuichi et al., "A Capacitive Fingerprint Sensor Chip Using Low-Temperature Poly-Si TFTs on a Glass Substrate and a Novel and Unique Sensing Method," IEEE Journal of Solid-State Circuits, IEEE Inc., New York, U.S. vol. 38, no. 2, February 2003, pages 274-280.					
	5	Inglis, C. et al., "A Robust, 1.8V 250μW Direct-Contact 500dpi Fingerprint Sensor", Solid-State Circuits Conference, 1998, Digest of Technical Papers. 1998 IEEE Intl. San Francisco, CA, USA 5-7 Feb. 1998, pgs. 284-285.					
EXAMINER				DATE CONSIDERED			
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

Date: July 28, 2005